



CDGI, Indore

**Chameli Devi School of Engineering**  
**Department of Electronics and communication**  
 With effect from 15 - 07- 2013



CLASS : V SEM ECE – A

ROOM NO: 231

Time Day	10:00 To 10:50	10:50 To 11:40	11:40 To 12:30	12:30 To 1:10	1:10 To 2:00	2:00 To 3:00	3.00 To 3:10	3:10 To 4:00	4:00 To 4:40
Monday	Control System (Mr. Rahul Katiyar)	Digital Communication (Ms. Abhilasha Gupta)	Comm. N/W & Trans. Lines (Mr. Ayaz Sheikh)	LUNCH BREAK	Comm. N/W & Trans. Lines (Mr. Ayaz Sheikh)	Microprocessors & Microcontrollers (Mr. C. A. Patil)	TEA BREAK	Aptitude (Mr. Sudhir Lambhate)	Voice & Data Comm (Mr. Ashish Gupta)
Tuesday	Voice & Data Comm (Mr. Ashish Gupta)	Control System (Mr. Rahul Katiyar)	Digital Communication (Ms. Abhilasha Gupta)		Aptitude (Mr. Sudhir Lambhate)	Soft Skills (Ms. Padmini Londhe)		Microprocessors & Microcontrollers Lab A1 (Mr. Gurendar Jandu) Digital Comm. Lab A2 (Mrs. Sonal Gupta, Ms. Abhilasha Gupta)	
Wednesday	Microprocessors & Microcontrollers (Mr. C. A. Patil)	Comm. N/W & Trans. Lines (Mr. Ayaz Sheikh)	Digital Communication (Ms. Abhilasha Gupta)		SEMINAR			Comm. N/W & Trans. Lines Lab A1 (Mr. Ayaz Sheikh, Mr. Aditya Sharma ) Microprocessors & Microcontrollers Lab A2 (Mr. Gurendar Jandu, Mr. Rahul Katiyar)	
Thursday	Soft Skills (Ms. Padmini Londhe)	Digital Comm. Lab A1 (Mrs. Sonal Gupta, Ms. Abhilasha Gupta) Software Lab- III Lab A2 (Mr. Mayank Srivastava, Ms. Hemant dubey )			Control System (Mr. Rahul Katiyar)	Microprocessors & Microcontrollers (Mr. C. A. Patil)		Technical Training ( Brajesh Kumar)	Voice & Data Comm (Mr. Ashish Gupta)
Friday	Voice & Data Comm (Mr. Ashish Gupta)	Software Lab- III Lab A1 (Mr. Mayank Srivastava, Ms. Hemant dubey ) Comm. N/W & Trans. Lines A2 (Mr. Ayaz Sheikh, Mr. Vinod Chouhan )			Control System (Mr. Rahul Katiyar)	Microprocessors & Microcontrollers (Mr. C. A. Patil)		Comm. N/W & Trans. Lines (Mr. Ayaz Sheikh)	Digital Communication (Ms. Abhilasha Gupta)

HOD

DEAN

Details	No of Slots
Theory	20
Laboratory	8
Aptitude/Soft Skills Training	5
Seminar	2
<b>Total Slots</b>	<b>35</b>



CDGI, Indore

**Chameli Devi School of Engineering****Department of ECE**

With effect from 15 - 07- 2013

**CLASS : V SEM ECE – B****ROOM NO: 230**

Time Day	10:00 To 10:50	10:50 To 11:40	11:40 To 12:30	12:30 To 1:10	1:10 To 2:00	2:00 To 3:00	3.00 To 3:10	3:10 To 4:00	4:00 To 4:40	
Monday	Comm. N/W & Trans. Lines (Mr. Vivek Trivedi)	Microprocessors & Microcontrollers (Ms. Shalini Sharma)	Technical training (Mr. Brijesh Kumar)	<b>LUNCH BREAK</b>	Control System (Mr. Animesh Masih)	Voice & Data comm. (Ms. Hemant Dubey)	<b>TEA BREAK</b>	Aptitude (Ms. Manisha Bagani)	Digital Comm. (Mrs. Sonal Gupta)	
Tuesday	Digital Comm. (Mrs. Sonal Gupta)	Digital Comm. Lab B1 (Mrs. Sonal Gupta, Mr. Harish Prajapati) Software Lab-III Lab B2 (Mr. Mayank Shrivastava)			Aptitude (Ms. Manisha Bagni)	Voice & Data comm. (Ms. Hemant Dubey)		Soft Skills (Mr. Sunil Chhapola)	Comm. N/W & Trans. Lines (Mr. Vivek Trivedi)	
Wednesday	Comm. N/W & Trans. Lines (Mr. Vivek Trivedi)	Digital Comm. (Mrs. Sonal Gupta)	Microprocessors & Microcontrollers (Ms. Shalini Sharma)		Control System (Mr. Animesh Masih)	Voice & Data comm. (Ms. Hemant Dubey)		Software Lab-III Lab B1 (Mr. Mayank Shrivastava) Digital Communication Lab B2 (Mrs. Sonal Gupta, Mr. Harish Prajapati)		
Thursday	Soft Skills (Mr. Sunil Chhapola)	Control System (Mr. Animesh Masih)	Microprocessors & Microcontrollers (Ms. Shalini Sharma)		Voice & Data comm. (Ms. Hemant Dubey)	Digital Comm. (Mrs. Sonal Gupta)		Microprocessors & Microcontrollers Lab B1 (Ms. Shalini Sharma) Comm. N/W & Trans. Lines Lab B2 (Mr. Vivek Trivedi, Mr. Vinod Chouhan)		
Friday	Comm. N/W & Trans. Lines (Mr. Vivek Trivedi)	Control System (Mr. Animesh Masih)	Microprocessors & Microcontrollers (Ms. Shalini Sharma)		Comm. N/W & Trans. Lines Lab B1 (Mr. Vivek Trivedi, Mr. Aditya Sharma) Microprocessors & Microcontrollers Lab B2 (Ms. Shalini Sharma, Mr. Harish Prajapati)			Seminar (Mr. Vinod Chouhan)		

**HOD****DEAN**

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Laboratory	8
Aptitude/Soft Skills Training	5
Seminar	2
<b>Total Slots</b>	<b>35</b>